UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,348,595 B2

Page 1 of 1

APPLICATION NO.: 10/828492

DATED

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INVENTOR(S)

: Sadami Takeoka et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title page, in Item "(54)" the title of the application, change "SEMICONDUCTOR WIRING SUBSTRATE, SEMICONDUCTOR DEVICE, METHOD FOR TESTING SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING SEMICONDUCTOR DEVICE" to -- SEMICONDUCTOR DEVICE AND METHOD FOR TESTING SEMICONDUCTOR DEVICE--;

In Item "(56) References Cited", change "2003/0008795 A1 01/2003 Asayama et al." to --2003/0006795 A1 01/2003 Asayama et al.--; and

change "2003/00322**8**3 A1 02/2003 Nagao et al." to --2003/0032263 A1 02/2003 Nagao et al.--.

> Signed and Sealed this Seventh Day of October, 2008

JON W. DUDAS Director of the United States Patent and Trademark Office